**SRM INSTITUTE OF SCIENCE AND TECHNOLOGY**

**CHENNAI**

**18CSC205J - OPERATING SYSTEMS**

**QUESTION BANK**

**UNIT – 3**

**4 MARKS**

1.Define address binding.

2.Define compile time.

3.Define execution time.

4.Define load time.

5.Define compiler , assembler and linker.

6.What is called as logical address and physical address?

7.Define dynamic loading.

8.Define dynamic linking and shared libraries.

9.Define overlay with example.

10. Define fixed size partition and multiple partition method.

11.Define first fit, best fit, worst fit algorithms.

12.Define external fragmentation and internal fragmentation.

13.Define compaction.

14.What is called as page and frames?

15.Define paging and segmentation.

16.Define PTBR and PTLR.

17.What is called as TLB?

18.Define TLB hit and TLB miss, hit ratio.

**12 MARKS**

1.Explain about how logical address and physical address are generated for a process along with neat diagram.

2.Explain about the two types of contiguous memory partitions and also explain the types of strategies used for selecting free holes in dynamic partition.

3.Explain paging concept along with neat diagram.

4.Explain different types of page table structures along with neat diagram.

5.Explain segmentation concept along with neat diagram.

6.Expalin the concept of segmentation with paging along with neat diagram.

7.Explain overlay concept along with example and neat diagram.

8. Given memory partition of 100 KB, 500 KB, 200 KB and 600 KB (in order). Show with neat Sketch how would each of the first-fit, best-fit and worst-fit algorithms place processes of 412 KB,317 KB, 112 KB and 326 KB(in order). Which algorithm is most efficient in memory allocation?